



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	G. Glenn Henry et al	Examiner:	Tonia Meonske
Docket:	CNTR.2022	Art Unit:	2183
Customer No.:	23669		
Serial No.:	09/849658		
Filing Date:	5/4/2001		
Title:	APPARATUS IN A MICROPROCESSOR FOR BRANCHING IN RESPONSE TO AN INSTRUCTION CACHE FETCH ADDRESS WITHOUT KNOWING WHETHER OR NOT A BRANCH INSTRUCTION IS PRESENT IN A CACHE LINE OF INSTRUCTION BYTES SELECTED FROM THE INSTRUCTION CACHE BY THE FETCH ADDRESS AND FOR CORRECTING IF THE BRANCHING WAS ERRONEOUS		

**AMENDMENT UNDER 37 CFR § 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**Introductory Comments**

In response to the Office action of January 6, 2005, please amend the above-identified application as follows.